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APPLICANTS

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** CONTINUING DATA *****

This application is a 371 of PCT/IB05/50153 01/13/2005

** FOREIGN APPLICATIONS *****

EUROPEAN PATENT OFFICE (EPO) 04100141.3 01/19/2004

** IF REQUIRED, FOREIGN FILING LICENSE GRANTED **

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Verified and Acknowledged	<input type="checkbox"/> Met after Allowance Initials	/JAMES C KERVEROS/ Examiner's Signature		NETHERLANDS	15	25	2

ADDRESS

NXP, B.V.
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TITLE

A test access architecture and method for modular testing a system on chip (SOC)

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